



Workshop On

INTELLECTUAL PROPERTY RIGHTS



December 07, 2009

Venue: C-DAC Noida Auditorium

B- 30, Institutional Area, Sector 62, Noida - 201307

Organized by

Centre for Development of Advanced Computing

(A Scientific Society of the Ministry of Communications and Information Technology, Govt. of India)

C-56/1, Institutional Area, Sector-62, Noida-201307

Phone: 91-120-2402551- 060 Fax: 91-120-2402569

Website: www.cdacnoida.in

About the Workshop

Intellectual Property Rights (IPR) plays a vital role in enhancing the socio – economic prosperity of the Country. Creativity and innovation are the new drivers of the world economy. With the increasing relevance of Intellectual Property (IP) in this era of globalization, it is important to recognize the role which IP systems play in the growth of the Nation. Thus, there is a need to create large public awareness about the pivotal role of IPR in globalized knowledge economy. With the view to create awareness on the significance of IPR to the people from various sectors in the Country, Centre for Development of Advanced Computing (C-DAC) is organizing a one day Workshop on Intellectual Property Rights at C-DAC, Noida.

The Workshop is intended to cover various aspects of IPR like Patents, Copyrights, Trademarks, etc. The technical sessions will be lead by the speakers having thorough knowledge in the subject who have been practicing in the field for many years. The expected participants of the programme include Scientists, Research Scholars, Small & Medium Enterprise (SME) and Grass Root Innovators.

About C-DAC

C-DAC as a premier initiative of the Department of Information Technology (DIT), Ministry of Communications and Information Technology (MC&IT), Government of India, embodies the national spirit of indigenous high-end research and development for state-of the-art technologies and solutions. In order to compete with contemporary IT Sectors and to bridge digital divide in Indian society, C-DAC has congregated its endemic human & technical resources towards the design, development & redeployment of Electronics & IT Products and Solutions. The core technology areas of C-DAC include High Performance Computing and Grid Computing; Multilingual and Heritage Computing; Professional Electronics encompassing VLSI, Embedded and Real Time Systems, Control Systems, Broadband and Wireless Systems, Strategic Electronics, Power Electronics, Agri-Electronics; Software Technologies, e-governance solutions and ICT for Development solutions; Cyber Security and Cyber Forensics; Health Informatics and Ubiquitous Computing.

Who can attend the Workshop?

Scientists, Research Scholars, Small & Medium Enterprise (SME) and Grass Root innovators.

Registration

- No registration fee will be charged for attending the Workshop.
- Those who wish to attend the workshop are requested to e-mail the filled registration form to ashutoshgupta@cdacnoida.in with subject line as "IPR Workshop" or send to C-

DAC Noida in the address given below on or before **30th November 2009**. Confirmation mails will be sent latest by 2nd December 2009.

- Since the number of participants are limited to 200, the participants will be selected based on first come- first serve basis.
- Last date for submission of Registration Form will be 30th November 2009.
- Spot registration is not entertained.

Travel and Accommodation

The participants have to make their own arrangements for travel and accommodation.

Agenda

Registration	9:00 AM - 09:30 AM
Welcome Address by Shri D. K. Jain (Director - Technical, C- DAC Noida)	9:30 AM - 09:40 AM
Inaugural Address by Dr. George Varkey (Executive Director, C- DAC Noida)	09:40 AM - 10:00 AM
Keynote Address – IPR - Patents, Copyrights (including violations) – an Overview by Dr. Padmavati M (Asst. Professor, Rajiv Gandhi School of Intellectual Property Law - IIT Kharagpur)	10:00 AM - 11:00 AM
Tea Break	11:00 AM - 11:20 AM
DIT initiatives on IPR Filing/Protection by Dr. A. K. Garg (Joint Director, DIT, Ministry of Communications and IT)	11:20 AM - 12:00 PM
Patenting System in India & International Filing by Dr. Rajesh Dixit (Head - Intellectual Property Office, Delhi & Assistant Controller of Patents & Design)	12:00 PM – 12:40 PM
Patents: Strategy and Process by Shri Arpit Chhabra (Director - Custom Research, Sagacious Research)	12:40 PM – 01.30 PM
Lunch	01:30 PM - 02:15 PM
How to Test Patentability of Your Invention Yourself by Shri Arpit Chhabra (Director - Custom Research, Sagacious Research)	02.15 PM - 03:00 PM

Tea Break	03:00 PM - 03:20 PM
Patent filing – Case Study and Hands on by Shri Arpit Chhabra (Director - Custom Research, Sagacious Research)	03:20 PM – 04:30PM
Concluding remarks by Dr. P. R. Gupta (Head – School of IT, C- DAC Noida)	04:30 PM – 04:50 PM
Vote of Thanks by Prof. P. N. Pandey (Head – Training, C- DAC Noida)	04:50 PM – 05:00 PM

Dates to remember

Last date of submission of Registration Form: 30th November 2009
Date of the Workshop: 7th December 2009

Address for Communication

Shri Ashutosh Gupta
Project Engineer, Embedded Systems
Email: ashutoshgupta@cdacnoida.in

Shri Vijayagopalan KP
Scientist 'E' & In - charge, Embedded Systems
Email: vgopal@cdacnoida.in

Centre for Development of Advanced Computing
C-56/1, Institutional Area, Sector-62,
Noida-201307



REGISTRATION FORM

Workshop on Intellectual Property Rights
(Organized by Centre for Development of Advanced Computing - Noida)

7th December 2009

Venue: C-DAC, Noida Auditorium, B- 30, Institutional Area, Sector 62, Noida - 201307

Name: _____

Designation: _____

Organization: _____

Address (Postal): _____

Telephone number: (O)_____ (M)_____

E-mail: _____

Nationality: _____

Signature: _____ Date: _____